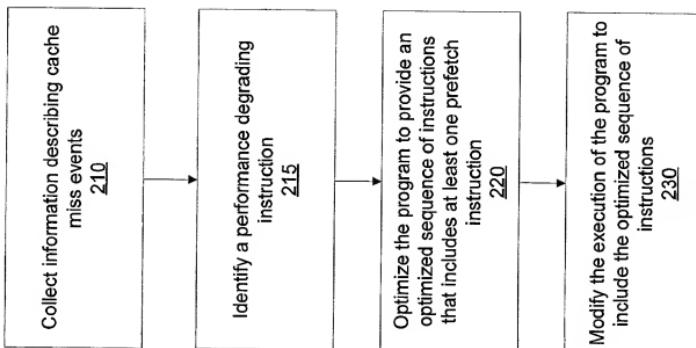


FIG. 1



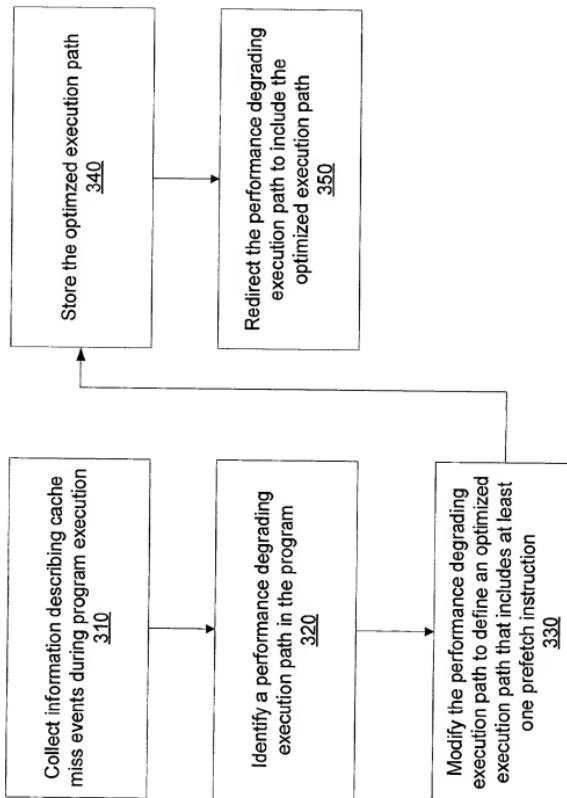


FIG. 3

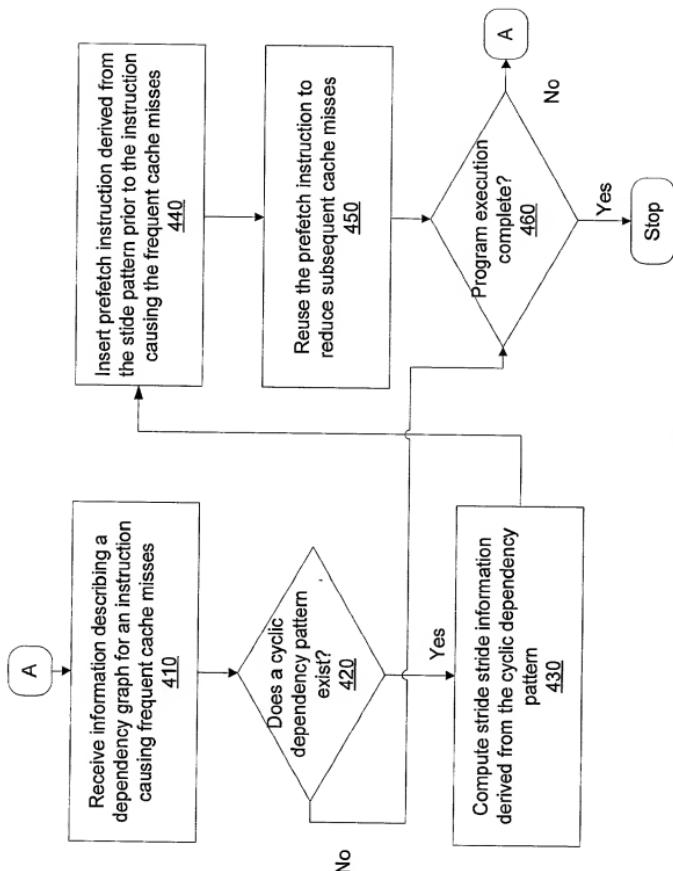


FIG. 4

510

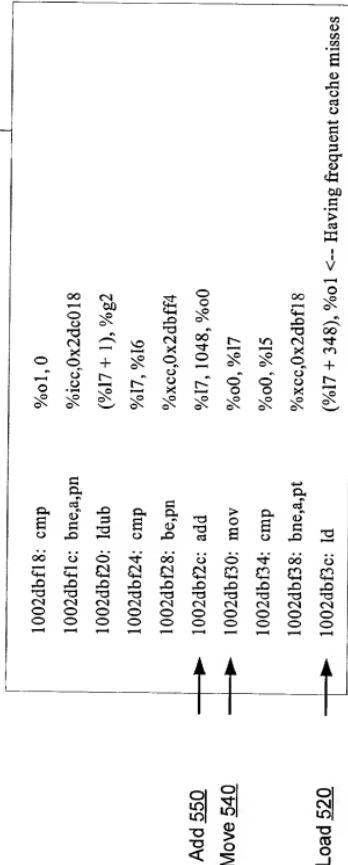


FIG. 5A

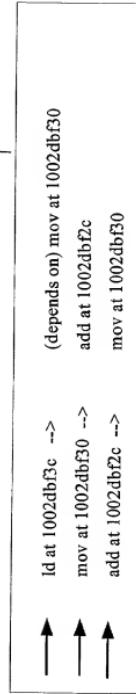


FIG. 5B

1002dbf18: cmp	%e01, 0
1002dbf1c: bne,apn	%ecc,0x2dc018 (%e07 + 1), %g2
1002dbf20: ldub	%e17, %e16
1002dbf24: cmp	%ecc,0x2dbff4
1002dbf28: be, pn	%e17, 1048, %e17
1002dbf2c: add	(%e07), %e01
1002dbf30: idx	%e00, %e15
1002dbf34: cmp	%ecc,0x2dbf18 (%e01 + 348), %e01 <--- Having frequent cache misses
1002dbf38: bne,ap,pt	
1002dbf3c: ld	

FIG. 5C

ld at 1002dbf3c	-> idx at 1002dbf30
idx at 1002dbf30	-> add at 1002dbf2c
add at 1002dbf2c	-> add at 1002dbf2c

FIG. 5D

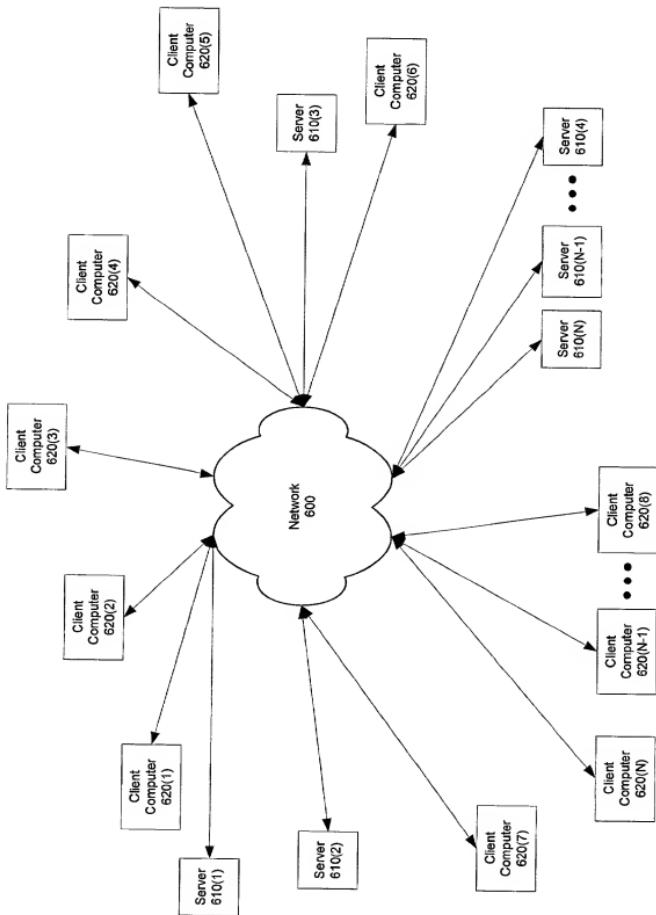


FIG. 6

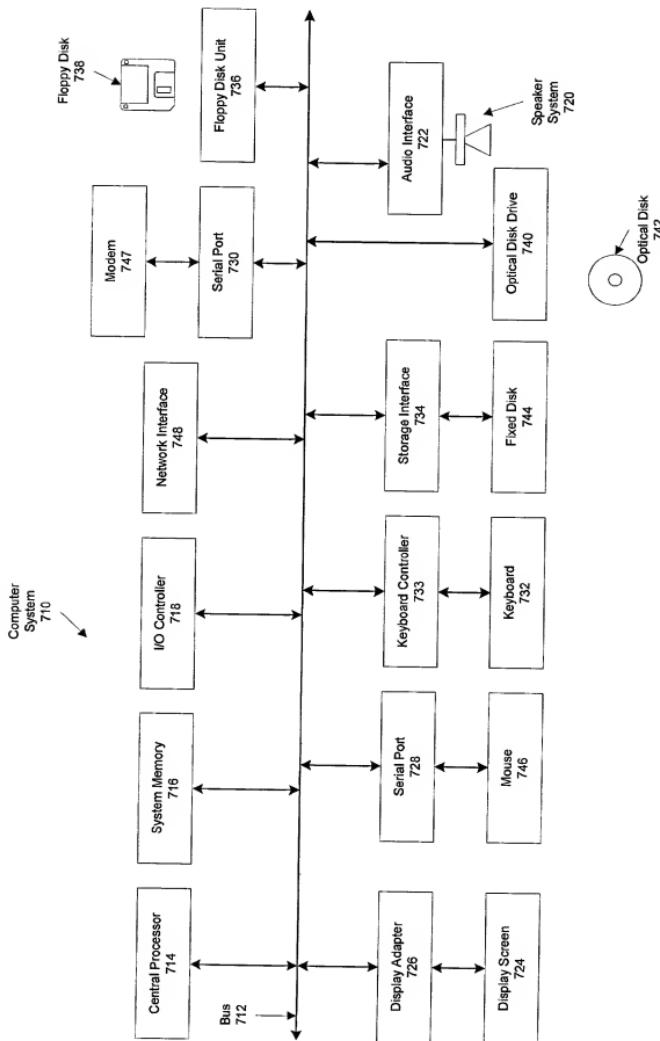
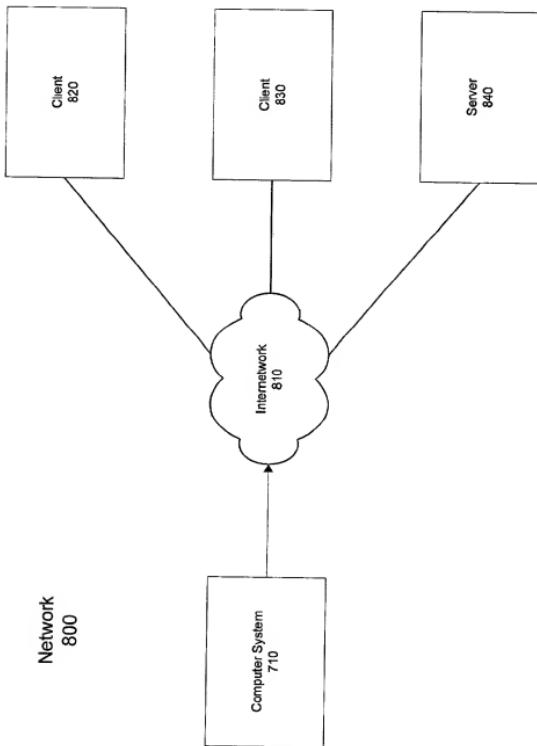


FIG. 7



F/G. 8